

REMARKS

At the outset, the Examiner is thanked for the thorough review and consideration of the subject application. The final Office Action of April 29, 2005 has been received and contents carefully reviewed.

By this Amendment, Applicants amend claims 5, 11, 16, 22, 30, 38 and 44. Accordingly, claims 1, 3-6, 8-19, 22-41, 43-49 and 51-53 are currently pending in the present application. Reexamination and reconsideration of the application are respectfully requested.

In the Office Action, the Examiner rejected claims 1, 3-6, 8-19, 22-41, 43-49 and 51-53 under 35 U.S.C. § 103(a) as being unpatentable over Okibayashi et al. (U.S. Patent No. 5,504,599) in view of Yokoyama et al. (U.S. Patent No. 6,507,379). Applicants respectfully traverse this rejection.

Claim 1 is allowable over the cited reference in that claim 1 recites a combination of elements including, for example, “an insulating layer on a first side of the first substrate; a light emitting structure including a light emitting layer on the insulating layer; a protective layer of an inorganic material on the light emitting structure... whereby the light emitting structure shares the first substrate with the TFT array structure.” None of the cited references, singly or in combination, teaches or suggests at least these features of the claimed invention. Accordingly, Applicants respectfully submit that claim 1 and claims 3-4 and 52-53, which depend therefrom, are allowable over the cited references.

In the Office Action, the Examiner cites Okibayashi et al. as teaching the aforementioned features in claim 1. Applicants respectfully disagree. Okibayashi et al. at Col. 6, lines 32-42 discloses,

“The EL light source 25 is a so-called thin film EL element, and is composed as described below. An insulation layer 26 is for preventing insulation breakdown of the EL light source 25, and is realized, for example, by silicone resin of 10 μm in thickness. The EL light source 25 is formed on an inside surface 27a of a box-shaped glass substrate 27, and the insulation layer 26 is formed so as to cover the EL light source 25. Thus, the end portion 27b of side wall of the glass substrate 27 on which the EL light source 25 is disposed, and the surface 26a of the insulation layer 26 are bonded to the other side 22b of the substrate 22.”

As disclosed in Okibayashi et al., the EL light source 25 is formed on a box-shaped glass substrate 27, not formed on the LCD substrate 22, and is then bonded to the LCD substrate 22. In contrast, the light emitting structure is formed on an LCD substrate 201a to share the LCD substrate 201a with the LCD device as recited in at least claim 1 of the present application.

Claim 5 is allowable over the cited reference in that claim 5 recites a combination of elements including, for example, “an organic light emitting element formed by interposing a first insulating layer on a first surface of the first substrate, wherein the first insulating layer is formed of an inorganic insulating material and the organic light emitting element is disposed in a region including a display region...” None of the cited references, singly or in combination, teaches or suggests at least these features of the claimed invention. Accordingly, Applicants respectfully submit that claim 5 and claims 6 and 8-10, which depend therefrom, are allowable over the cited references.

Claim 11 is allowable over the cited reference in that claim 11 recites a combination of elements including, for example, “forming a light emitting structure including a light emitting layer on the inorganic insulating layer, wherein the light emitting structure is disposed in a region including a display region ... whereby the light emitting structure shares the first substrate with the TFT array structure.” None of the cited references, singly or in combination, teaches or suggests at least these features of the claimed invention. Accordingly, Applicants respectfully submit that claim 11 and claims 12-15, which depend therefrom, are allowable over the cited references.

Claim 16 is allowable over the cited reference in that claim 16 recites a combination of elements including, for example, “forming a light emitting element including a light emitting layer on the first insulating layer, wherein the first surface is opposite to the second surface and the light emitting element is disposed in a region including a display region... whereby the organic light emitting element shares the first substrate with the TFT array element.” None of the cited references, singly or in combination, teaches or suggests at least these features of the claimed invention. Accordingly, Applicants respectfully submit that claim 16 and claims 17-19, which depend therefrom, are allowable over the cited references.

Claim 22 is allowable over the cited reference in that claim 22 recites a combination of elements including, for example, “a first substrate having a first surface and a second surface,

wherein the first surface is opposite to the second surface, the first surface has a driving element, and the second surface has a light emitting structure disposed in a region including a display region...a liquid crystal material interposed between the first substrate and the second substrate, wherein a first insulating layer is interposed between the second surface of the first substrate and the light emitting structure, and the first and second substrates perform an additional function of polarization.” None of the cited references, singly or in combination, teaches or suggests at least these features of the claimed invention. Accordingly, Applicants respectfully submit that claim 22 and claims 23-29, which depend therefrom, are allowable over the cited references.

Claim 30 is allowable over the cited reference in that claim 30 recites a combination of elements including, for example, “forming a light emitting structure on the first insulating layer, the light emitting structure disposed in a region including a display region... wherein the light emitting structure shares the first substrate with the thin film transistor array, and the first and second substrates perform an additional function of polarization.” None of the cited references, singly or in combination, teaches or suggests at least these features of the claimed invention. Accordingly, Applicants respectfully submit that claim 30 and claims 31-37, which depend therefrom, are allowable over the cited references.

Claims 38-41 and 43 are allowable over the cited references in that claims 38-41 and 43 recite a combination of elements including, for example, “a thin film protective layer of an inorganic material on the light emitting structure... whereby the light emitting structure shares the first substrate with the TFT array structure.” None of the cited references teaches or suggests at least this feature of the present invention.

Claim 44 is allowable over the cited reference in that claim 44 recites a combination of elements including, for example, “forming a light emitting structure on the first insulating layer in a region including a display region... whereby the light emitting structure shares the first substrate with the TFT array structure.” None of the cited references, singly or in combination, teaches or suggests at least these features of the claimed invention. Accordingly, Applicants respectfully submit that claim 44 and claims 45-49 and 51, which depend therefrom, are allowable over the cited references.

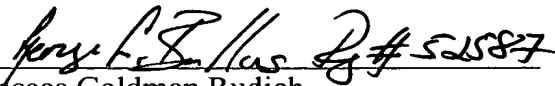
Applicants believe the application is in condition for allowance and early, favorable action is respectfully solicited. If the Examiner deems that a telephone conference would further

the prosecution of this application, the Examiner is invited to call the undersigned attorney at the telephone number (202) 496-7500. All correspondence should continue to be sent to the below-listed address.

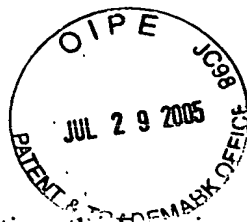
If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time, or any other fees required to complete the filing of this response, may be charged to Deposit Account No. 50-0911. Please credit any overpayment to deposit Account No. 50-0911.

Dated: July 29, 2005

Respectfully submitted,

By 
Rebecca Goldman Rudich
Registration No.: 41,786
MCKENNA LONG & ALDRIDGE LLP
1900 K Street, N.W.
Washington, DC 20006
(202) 496-7500
Attorney for Applicant

Application No.: 09/891,534



Docket No.: 8733.469.00-US

the prosecution of this application; the Examiner is invited to call the undersigned attorney at the telephone number (202) 496-7500. All correspondence should continue to be sent to the below-listed address.

If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time, or any other fees required to complete the filing of this response, may be charged to Deposit Account No. 50-0911. Please credit any overpayment to deposit Account No. 50-0911.

Dated: July 29, 2005

Respectfully submitted,

By Rebecca Goldman Rudich 2#52587
Rebecca Goldman Rudich
Registration No.: 41,786
MCKENNA LONG & ALDRIDGE LLP
1900 K Street, N.W.
Washington, DC 20006
(202) 496-7500
Attorney for Applicant